

PAT-NO: JP402105683A  
DOCUMENT-IDENTIFIER: JP 02105683 A  
TITLE: PICTURE ELEMENT DEFECT CORRECTION CIRCUIT

---

**Abstract Text - FPAR (1):**

**PURPOSE:** To improve picture quality by providing a memory to store the defective position of an picture element, a timing signal generator, and an amplifier to increase the output of a CCD sensor as twice that in an ordinary operation, and performing the correction of the defect of the output of a defective photodiode.

**Abstract Text - FPAR (2):**

**CONSTITUTION:** A signal outputted from a two-dimensional CCD image sensor 1 having a defect of one bit is amplified at the amplifier 4, and is outputted from an output terminal 5. At this time, an output signal from the CCD sensor including the output of the defective photodiode 7 is generated from the timing generation circuit 3 which generates a timing signal based on the memory 2 which stores the defective position of the picture element and data in the memory. The amplifier 4 amplifies the output signal from the CCD sensor including the output of the defective photodiode 7 at the timing of the defective photodiode with a gain as twice an ordinary gain. therefore, it is possible to interpolate the signal of defective photodiode.

**Document Identifier - DID (1):**

JP 02105683 A

**Inventor Name - INNI (1):**

HAYASHI, TOMOAKI

**Inventor Name ( Derived ) - INZZ (1):**

HAYASHI, TOMOAKI